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G43T-DM1

Rev: A00
Support EuP Lot 6

The EAGLELAKE module Specification :

- 1. MEMORY : 2 channel/4 DIMM DDR3 socket
- 2. SLOT : 1 PCI-E by 16 / 2 PCI-E by 1 /1 PCI
- 3. With 10 port Usb 2.0 & 4 port S-ATA 2
- 4. LAN : Relteak 10/100/1000M LAN
- 5. AUDIO : REALTEK ALC888S Audio Codec
- 6. I/O : Super I/O ITE 8755
- 7. VIDEO : VGA & HDMI

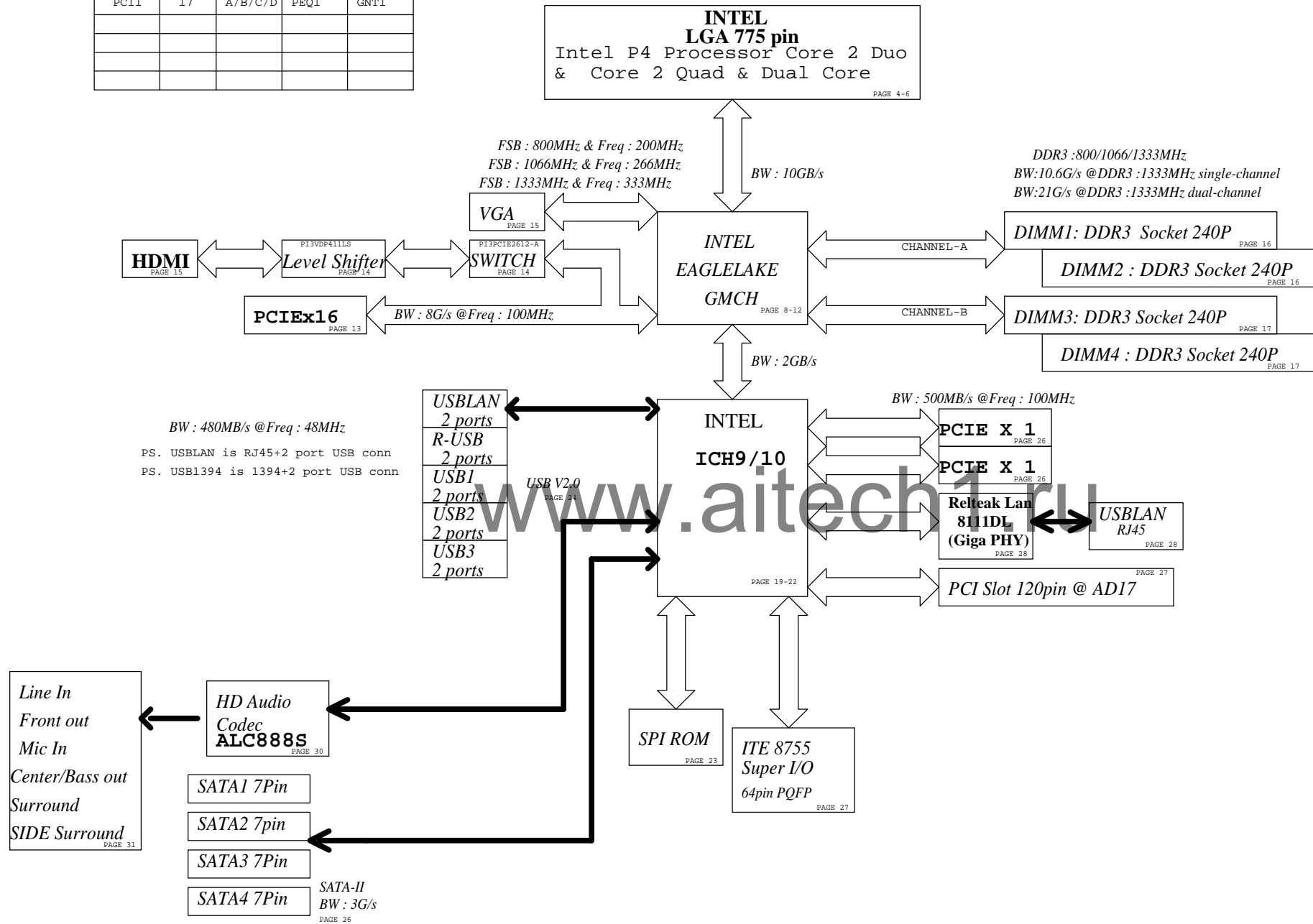
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McCreary(Corporate) ,vPro
EagleLake Q: Corporate Integrated Graphics

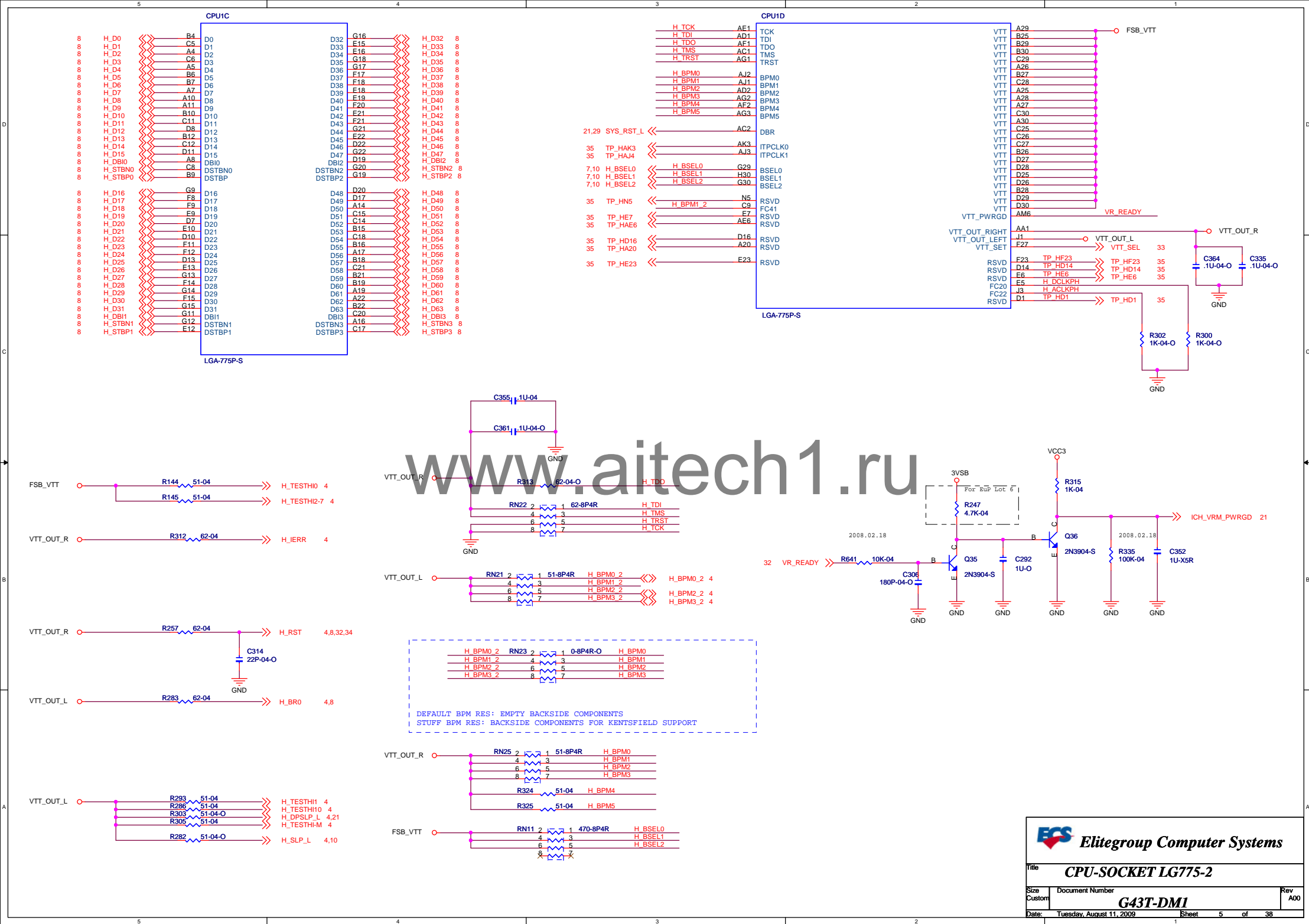
Boulder Creek(consumer) ,VIIV
* EagleLake G: Consumer Integrated Graphics
EagleLake P: Consumer Discrete Graphics

PCB	Total Thickness	Core Prepreg
BearLake	62 mil	50 mil
* EagleLake	59 mil	47 mil

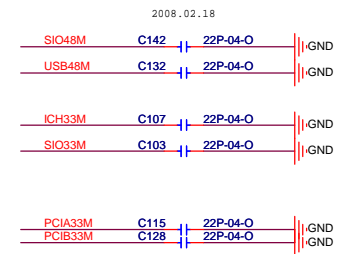
DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	17	A/B/C/D	PEQ1	GNT1



PCB : 244 x 244 x 1.5 mm ; 4 layers



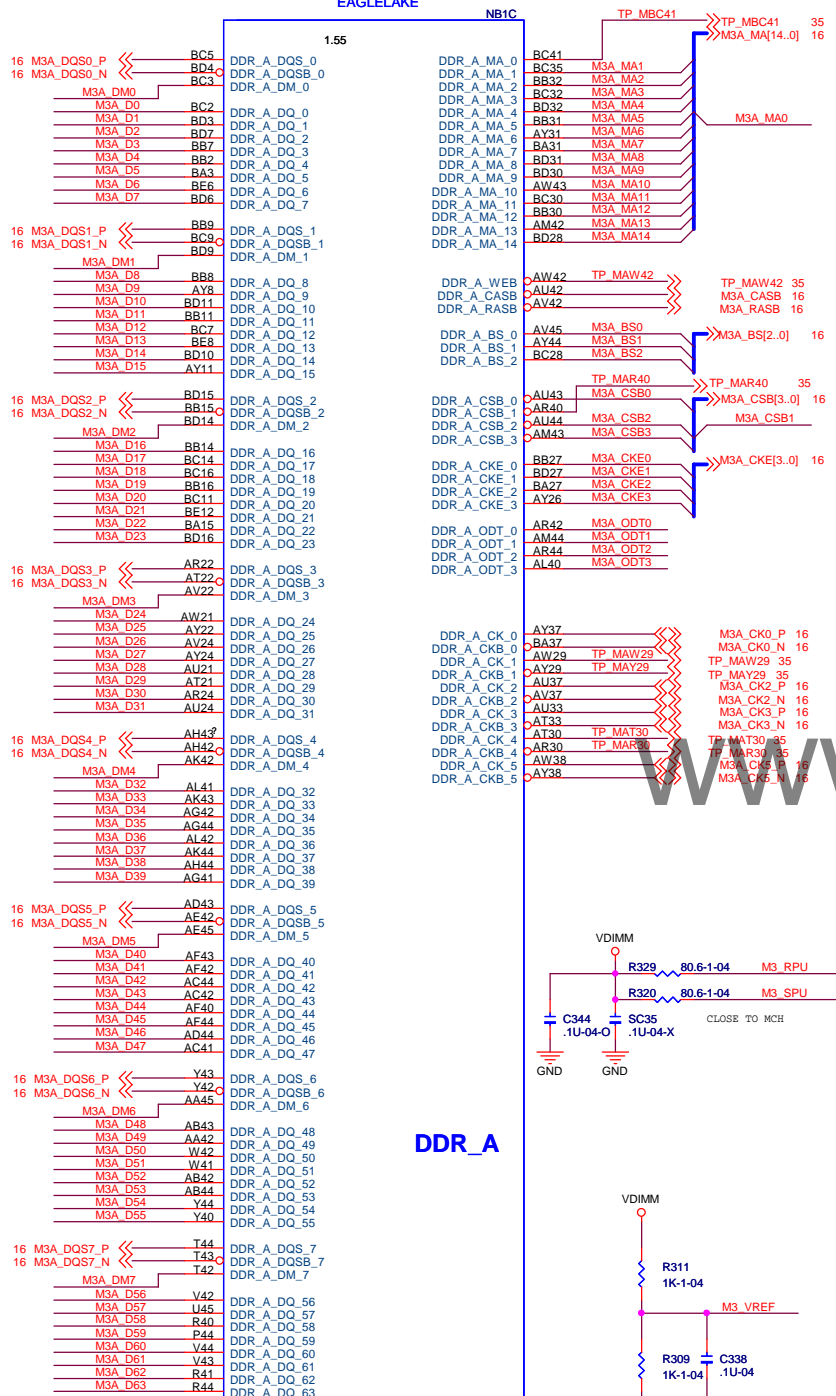
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Bit2 FSLC	Bit1 FSLB	Bit0 FSLA	CPU CLOCK (MHZ)
0	0	0	266.66
0	0	1	133.33
0	1	0	200.00
0	1	1	166.66
1	0	0	333.33
1	0	1	100.00
1	1	0	400.00

EAGLELAKE

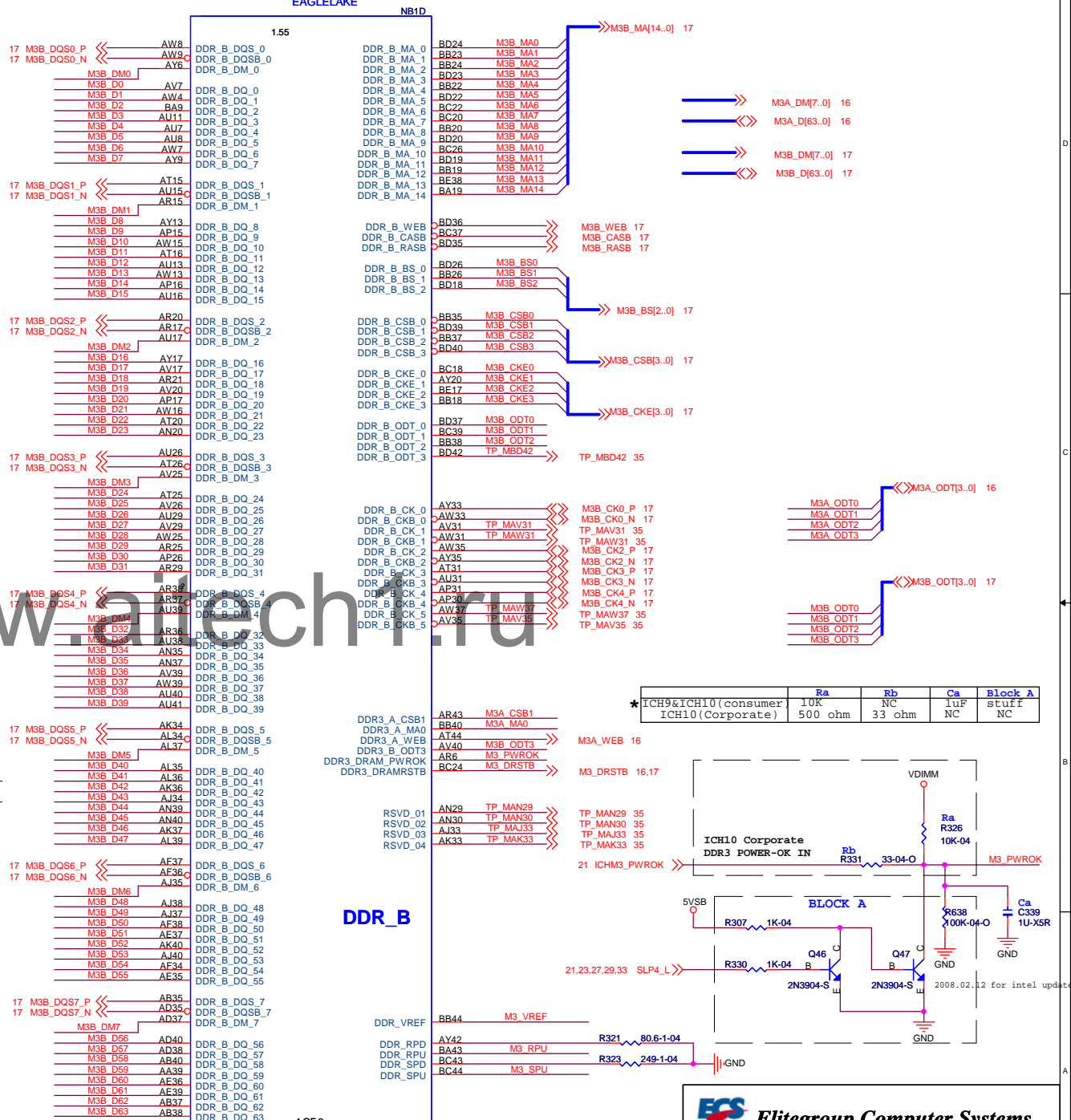
NB1C



AC82G45S A3(EAGLELAKE)

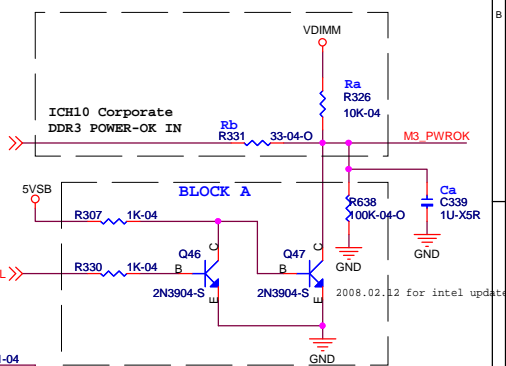
EAGLELAKE

NB1D



AC82G45S A3(EAGLELAKE)

	Ra	Rb	Ca	Block A
* ICH9&ICH10 (consumer)	10K	NC	1uF	stuff
ICH10 (Corporate)	500 ohm	33 ohm	NC	NC

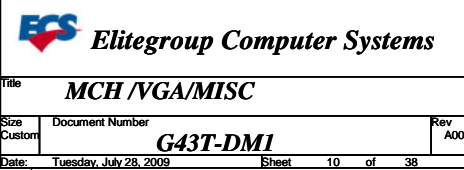


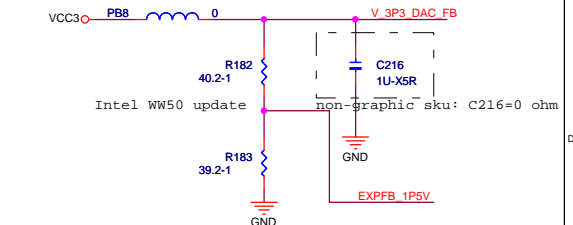
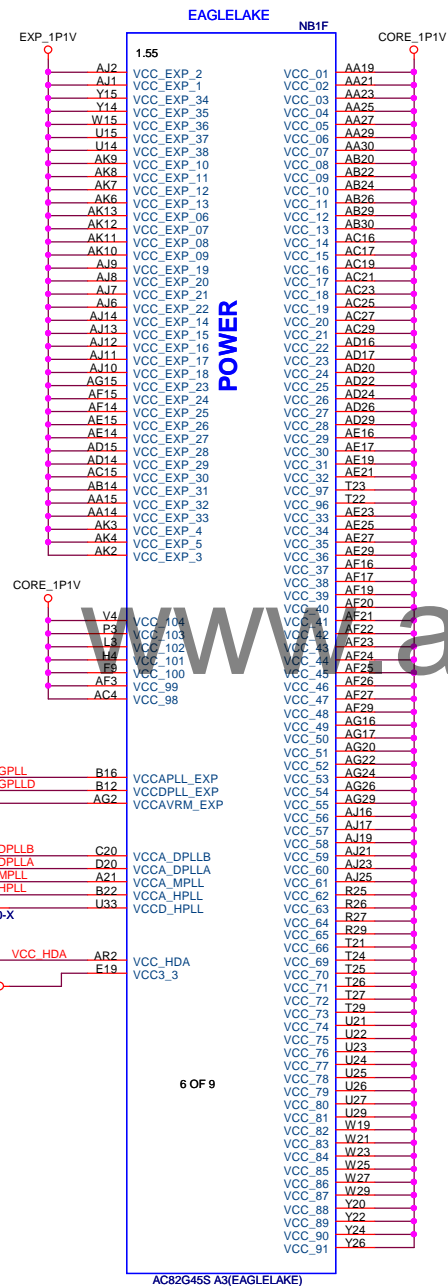
Elitegroup Computer Systems

Title **MCH /DDR III 1/DDR III 2**

Size Document Number **G43T-DM1**

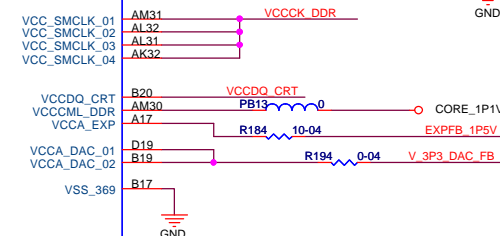
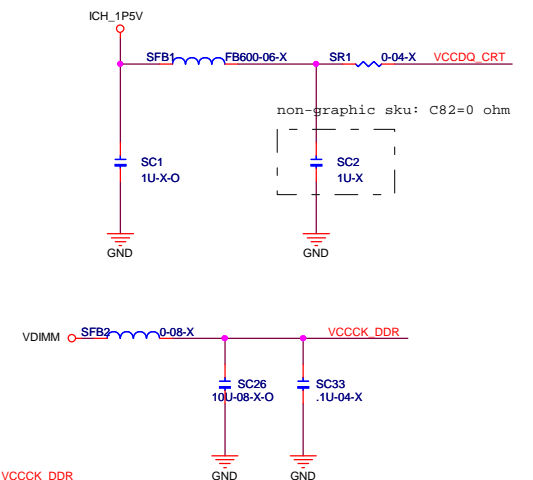
Date: Tuesday, July 28, 2009 Sheet 9 of 38

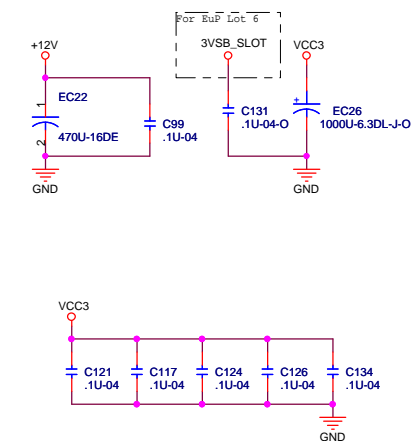
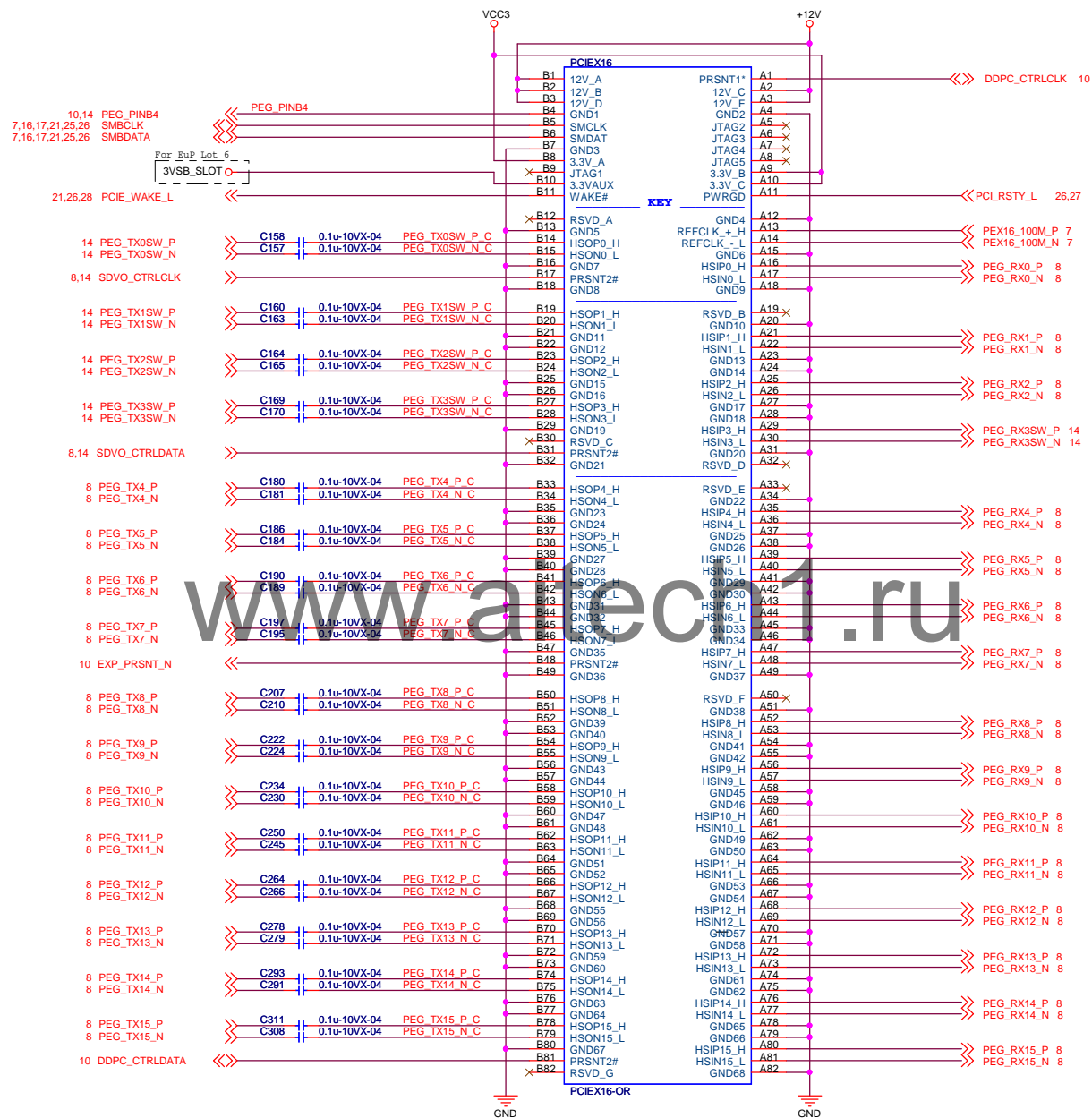


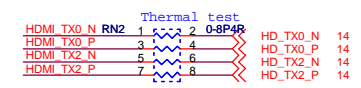
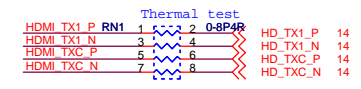
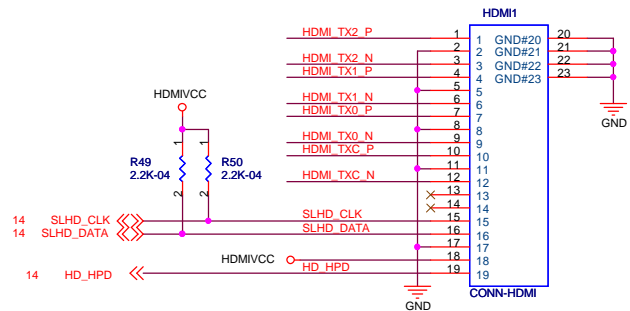
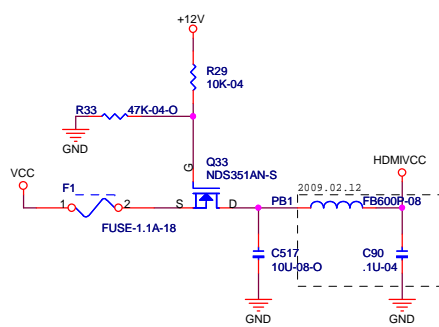


EAGLELAKE SQU TYPE	
Q SQU	RE==NC
G SQU	RE==NC
P SQU	RE==0 ohm

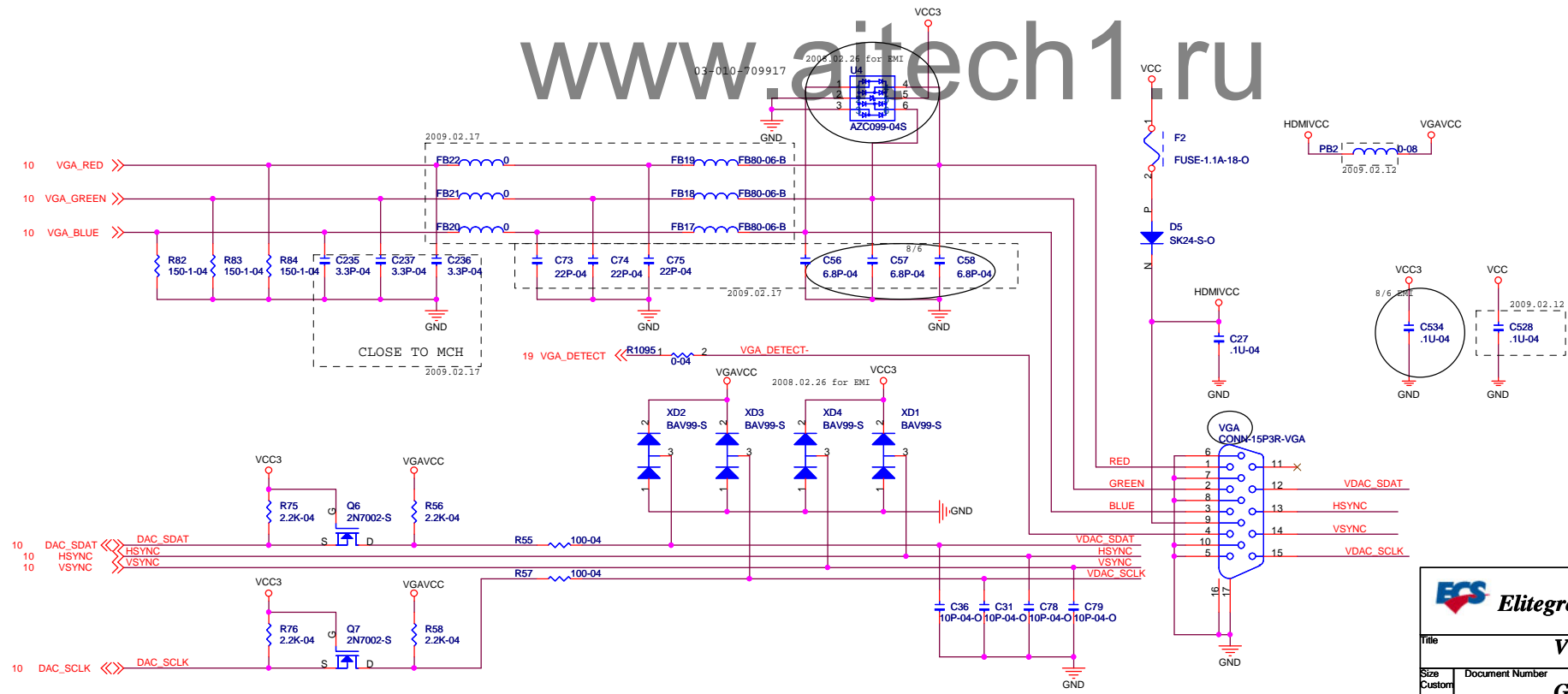
* Intel WW43 UPDATA

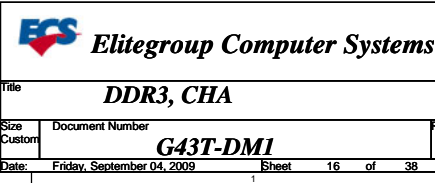


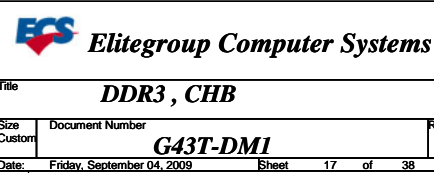


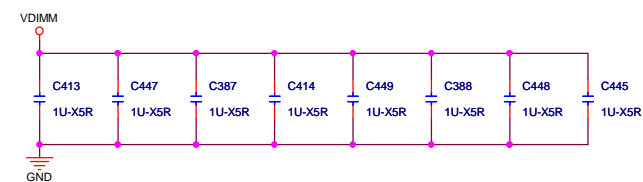
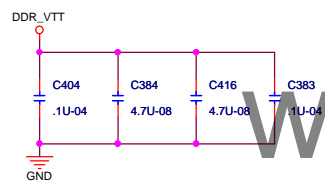
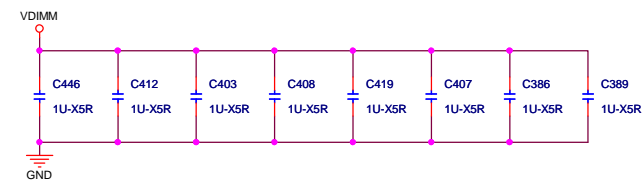
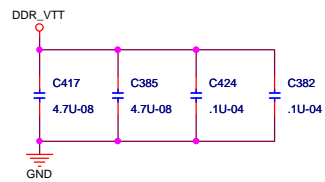


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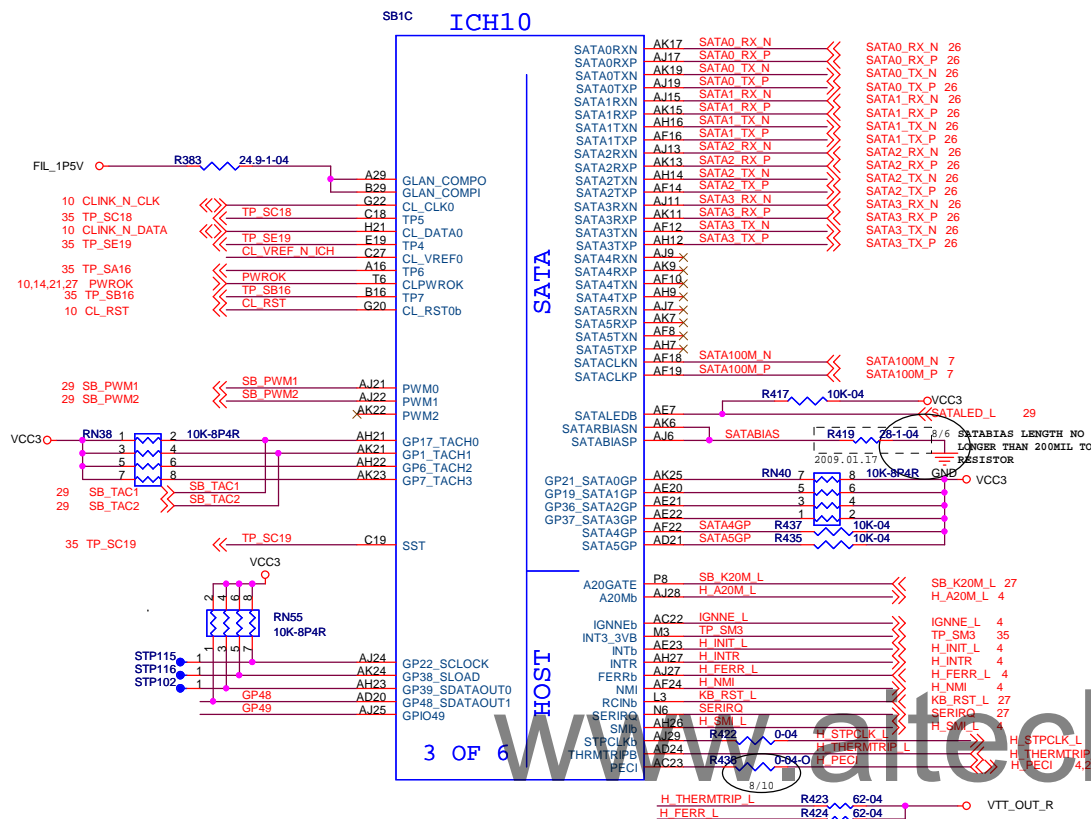








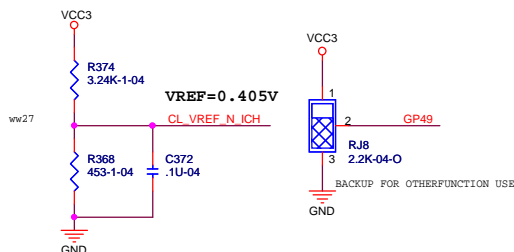
www.aitech1.ru



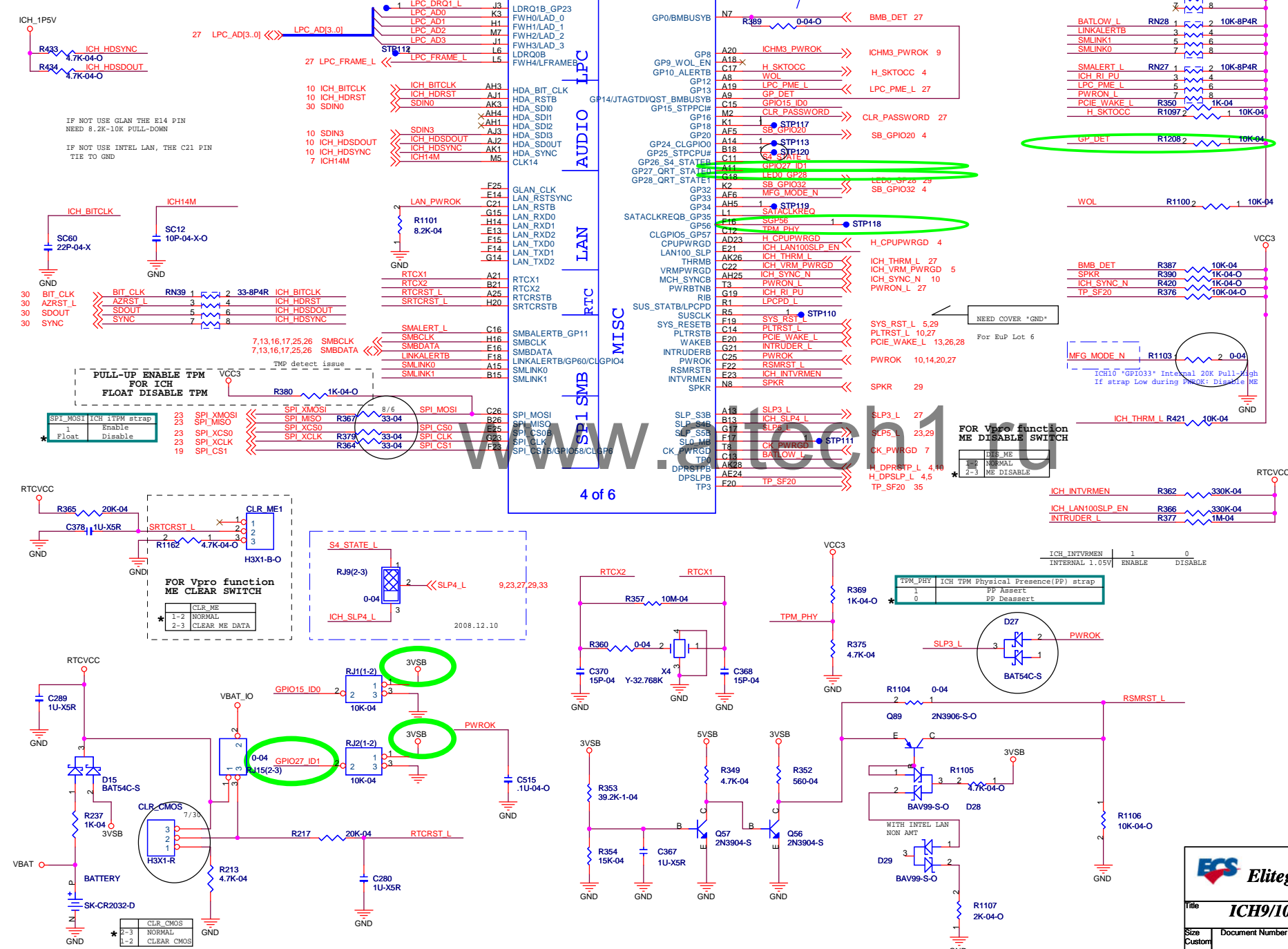
If without use pins L3,P8,N6
Please (4.7K-10K) pull-up to VCC.
Don't let it floating

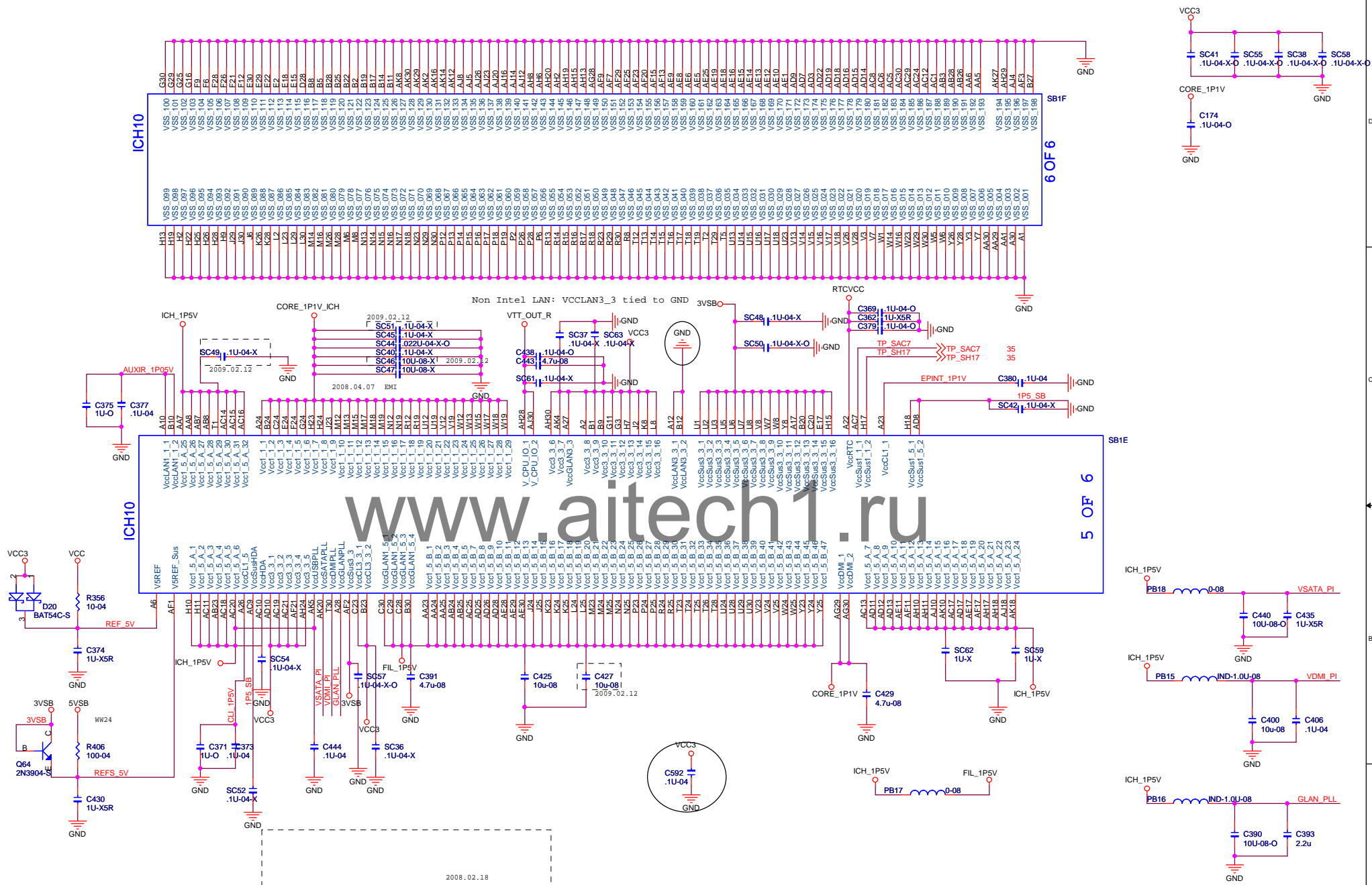
(ICHSTRP0:bit 5) to 0(default)
CL_VREF0 pin left as no connect

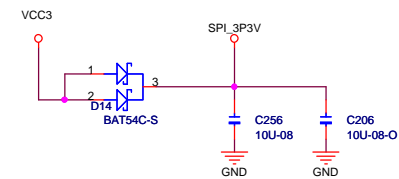
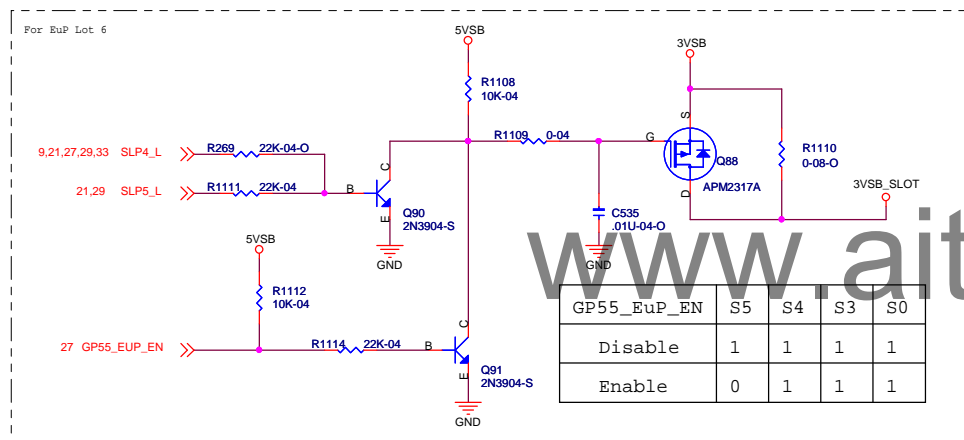
(ICHSTRP0:bit 5) must be set to 1
External Vref may be optionally
implemented



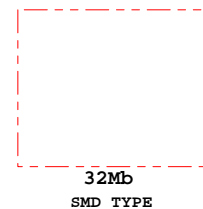
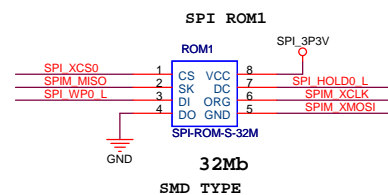
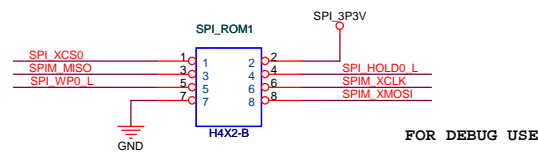
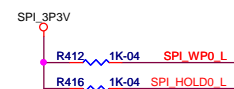
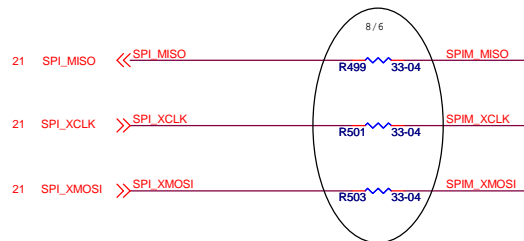
	ICH 10,C12 pin	ICH 10,C26 pin	MCH,L17 pin
Disable	Pulldown	Float	Float
Enable	Pullhi	Pullhi	Pulldown



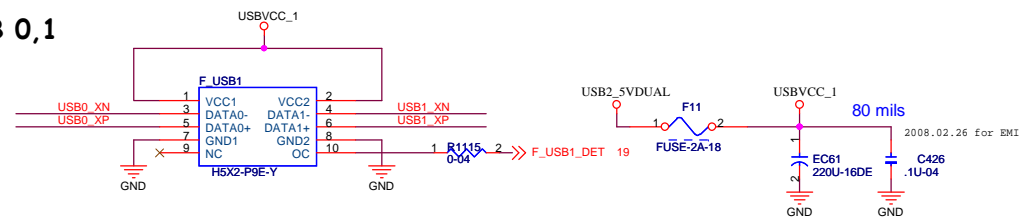




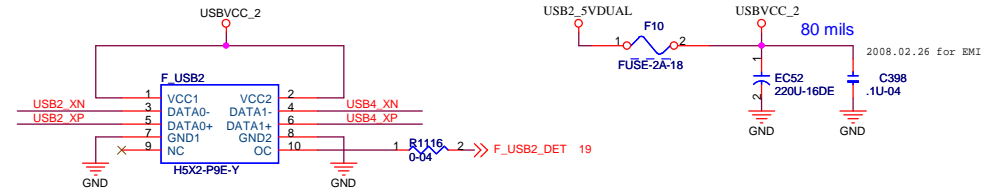
21 SPI_XCS0 >> SPI_XCS0



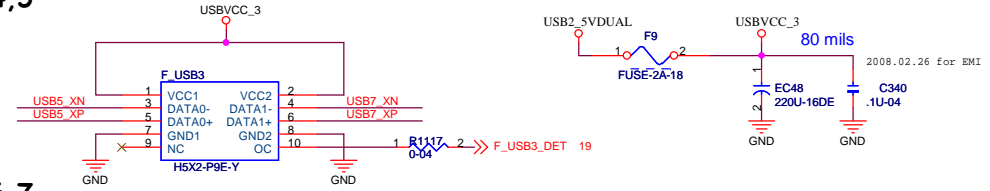
USB 0,1



USB 2,3



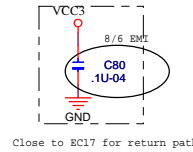
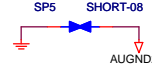
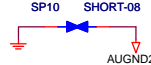
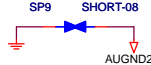
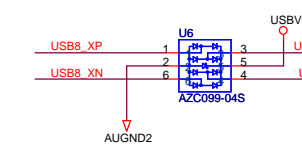
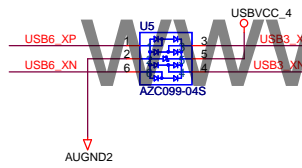
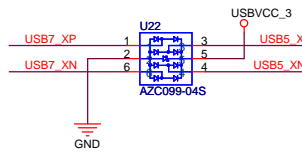
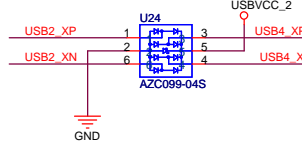
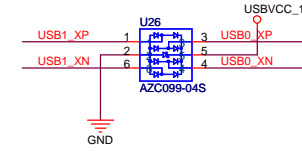
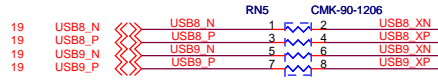
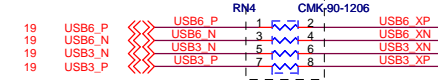
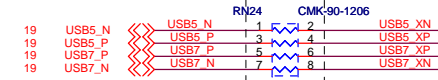
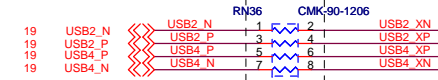
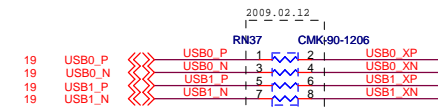
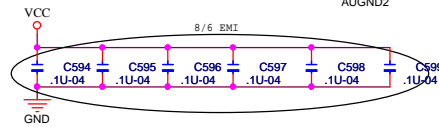
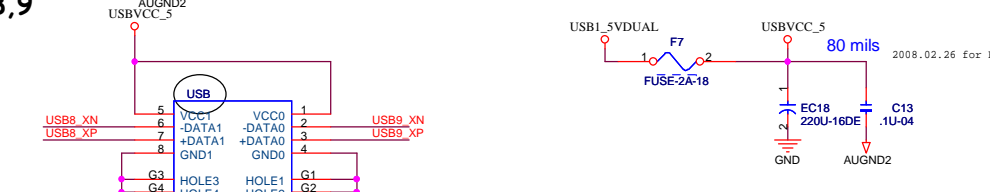
USB 4,5



USB 6,7

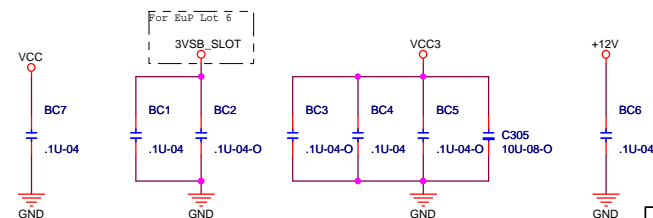
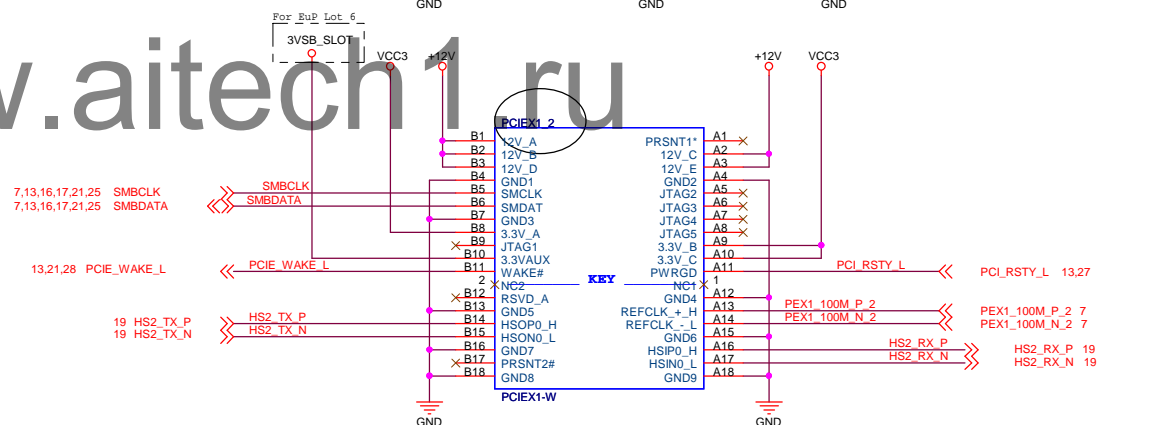
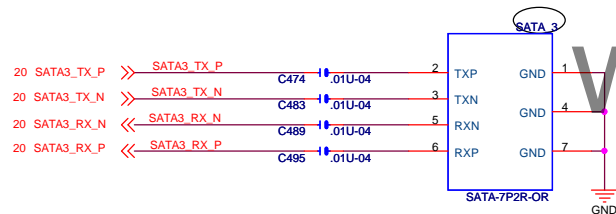


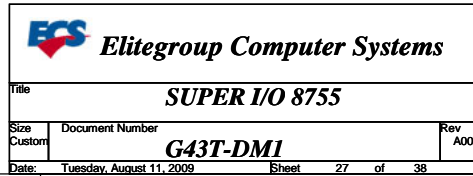
USB 8,9

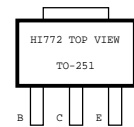
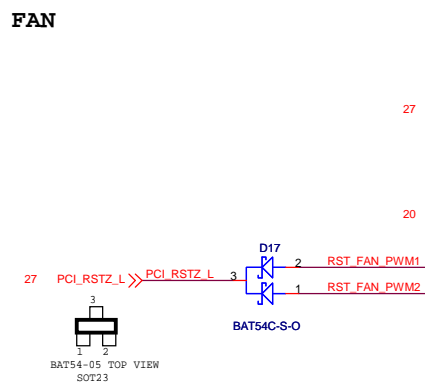
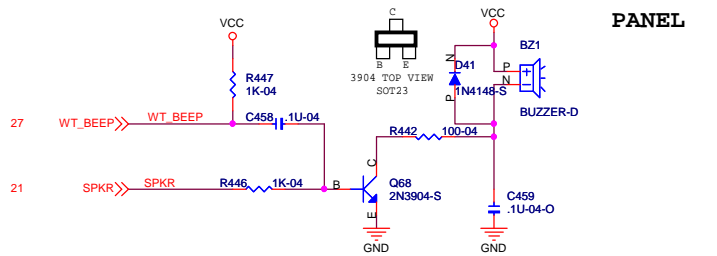




<i>Elitegroup Computer Systems</i>			
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Size Custom	Document Number <i>G43T-DM1</i>		Rev A00
Date:	Tuesday, August 11, 2009	Sheet 25 of 38	



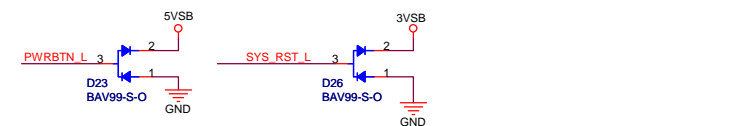




Top Veiw

Power status	Power LED
S0	Blue steady
S1/S3	Blue blinking
S4/S5	OFF
Status	Storage LED
Active	Blue blinking
Idle	OFF
Status	LAN LED
Active	Blue blinking
Idle	OFF

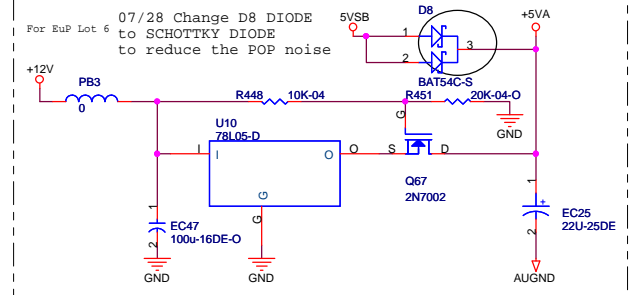
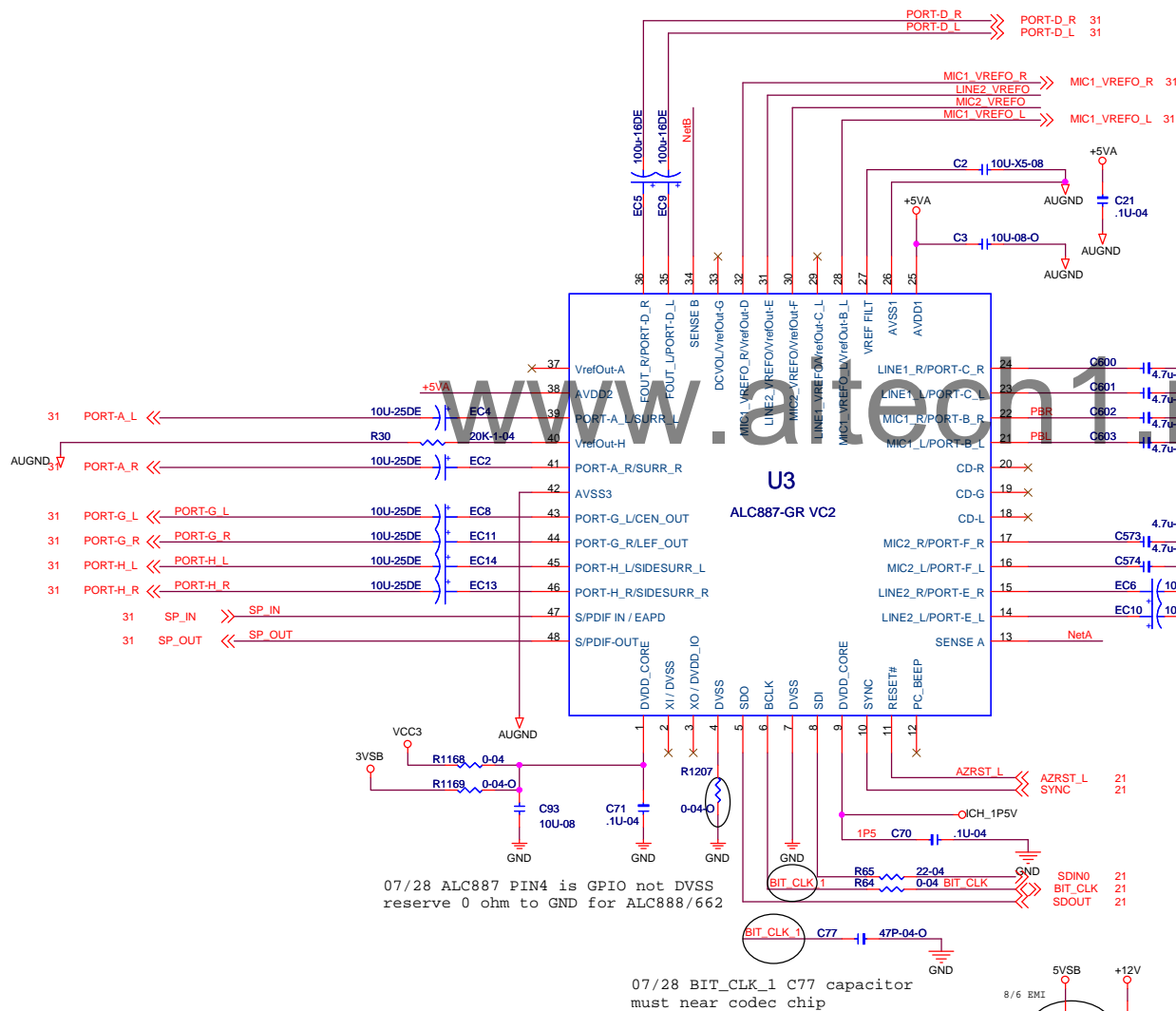
ID0 Chassis type
0 535s Slim tower
1 535 Mini tower



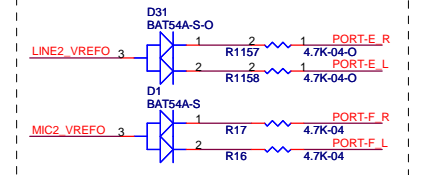
BACKUP FROM SIO GPIO

FOR F/W SET

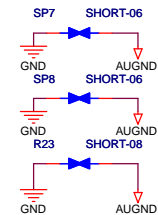
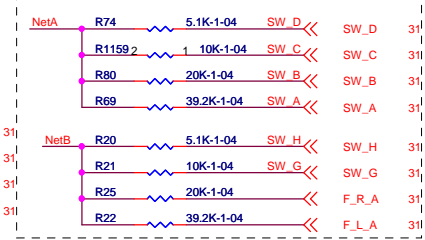
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Size Custom	Document Number G43T-DM1	Rev A00
Date: Tuesday, July 28, 2009	Sheet 29	of 38



Place near Chip



Resistors Networks



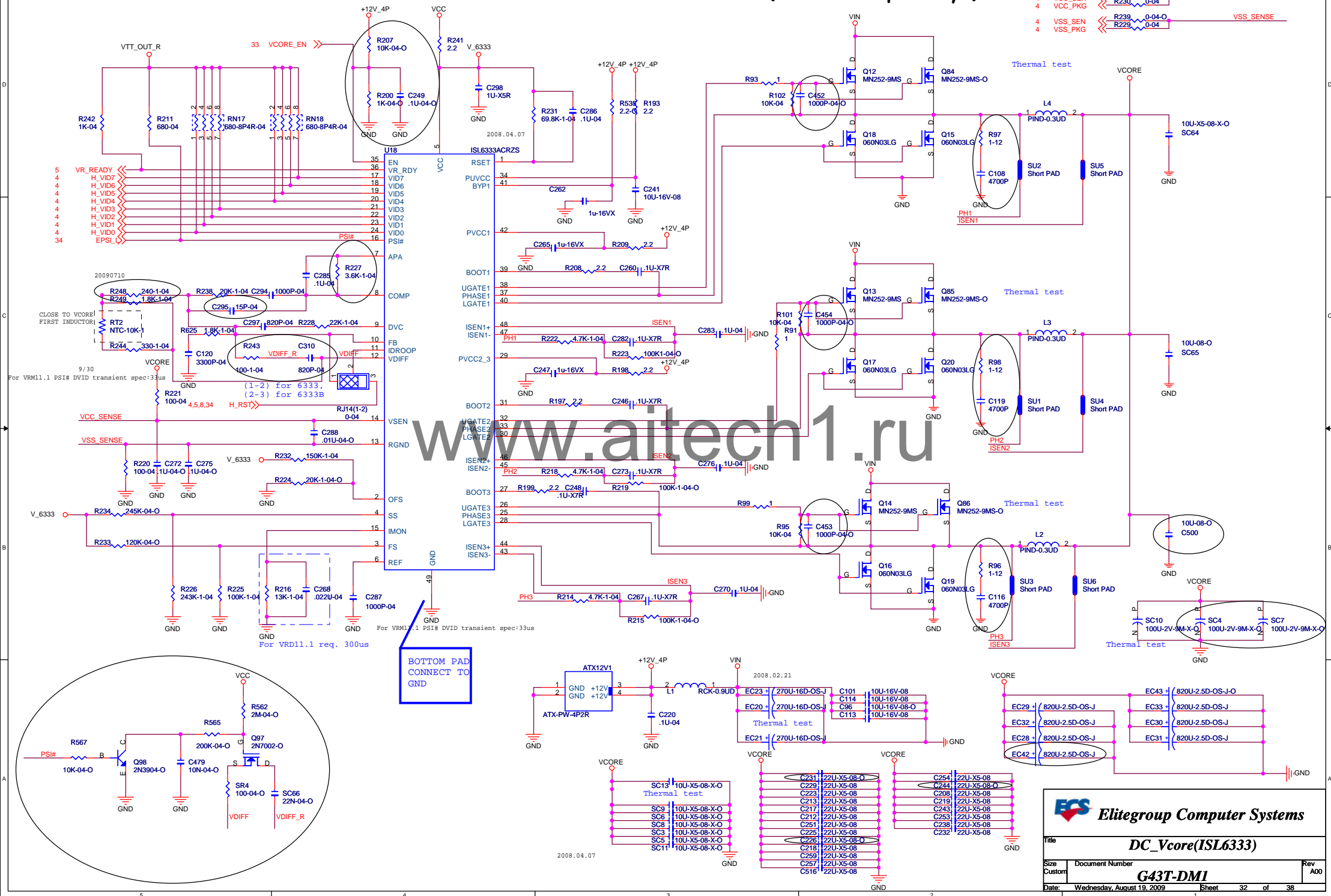
Elitegroup Computer Systems

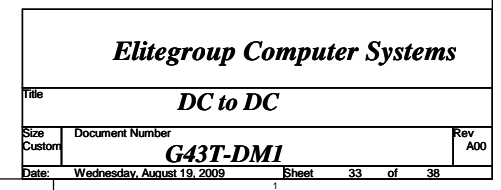
Title: **Audio CODEC(ALC888S)**

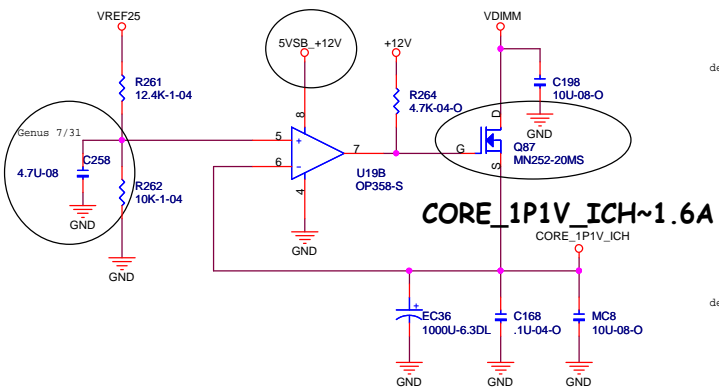
Size: Document Number **G43T-DM1** Rev: A00

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ISL6333CR FOR VRD11.1 POWER CKT (Low frequency)







If stuff Ra

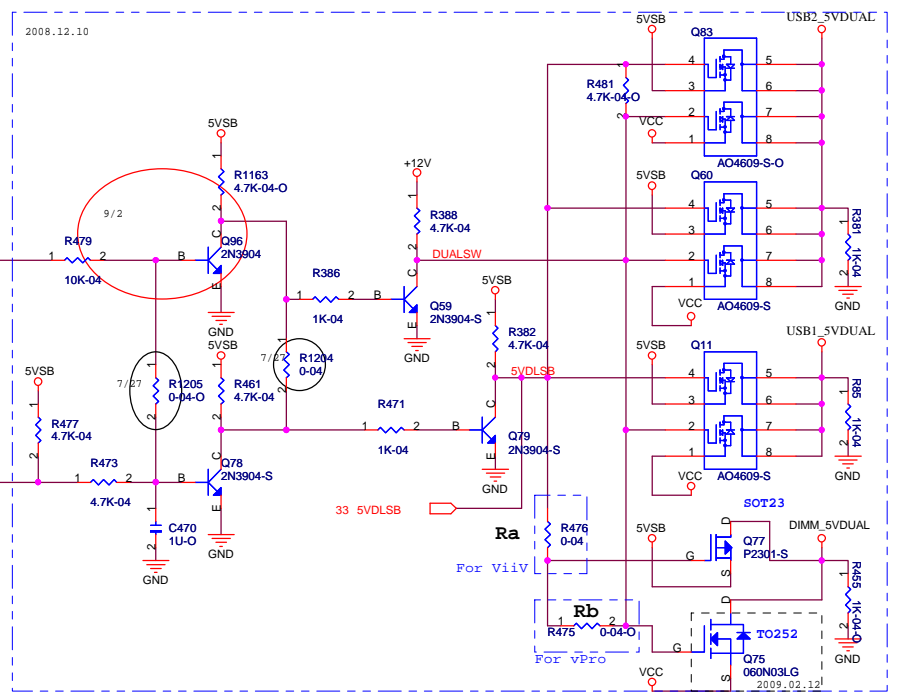
Status	Target			Super I/O 8720-CX
	USB2_5VDUAL	USB1_5VDUAL	DIMM_5VDUAL	
AC plug	0	0	0	GPIO_S4_S5=OD=1
S0,S1	VCC	VCC	VCC	GPIO_S4_S5=1
S3	5VSB	5VSB	5VSB	GPIO_S4_S5=0
S4,S5	5VSB	5VSB	5VSB	GPIO_S4_S5=0
S4,S5	0	0	0	GPIO_S4_S5=1

default *

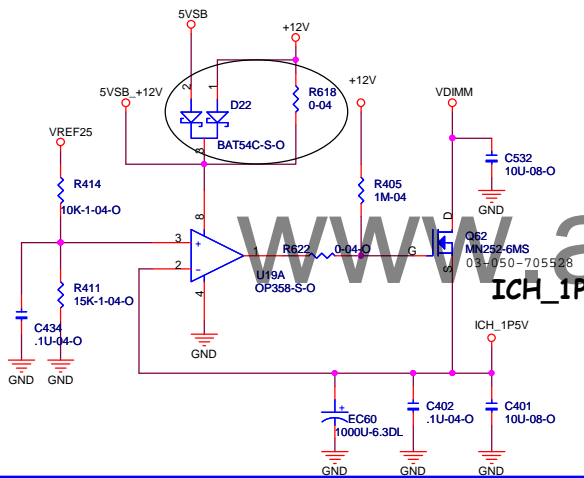
If stuff Rb

Status	Target			Super I/O 8720-CX
	USB2_5VDUAL	USB1_5VDUAL	DIMM_5VDUAL	
AC plug	0	0	5VSB	GPIO_S4_S5=OD=1
S0,S1	VCC	VCC	VCC	GPIO_S4_S5=1
S3	5VSB	5VSB	5VSB	GPIO_S4_S5=0
S4,S5	5VSB	5VSB	5VSB	GPIO_S4_S5=0
S4,S5	0	0	5VSB	GPIO_S4_S5=1

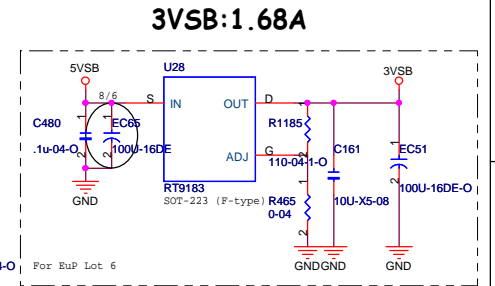
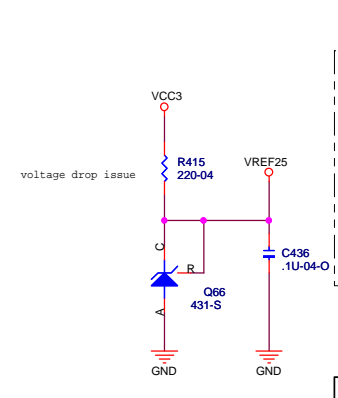
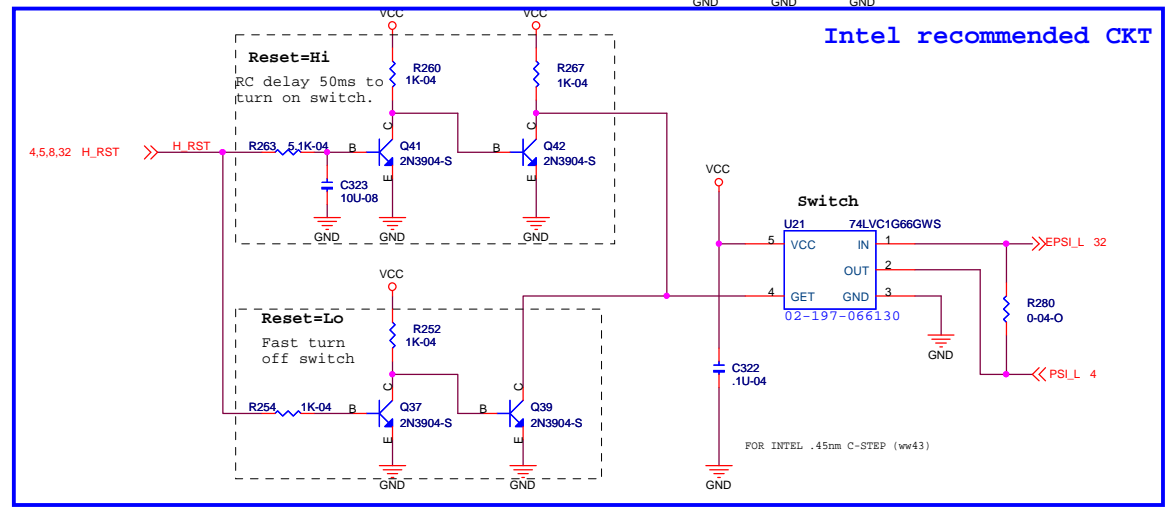
default *

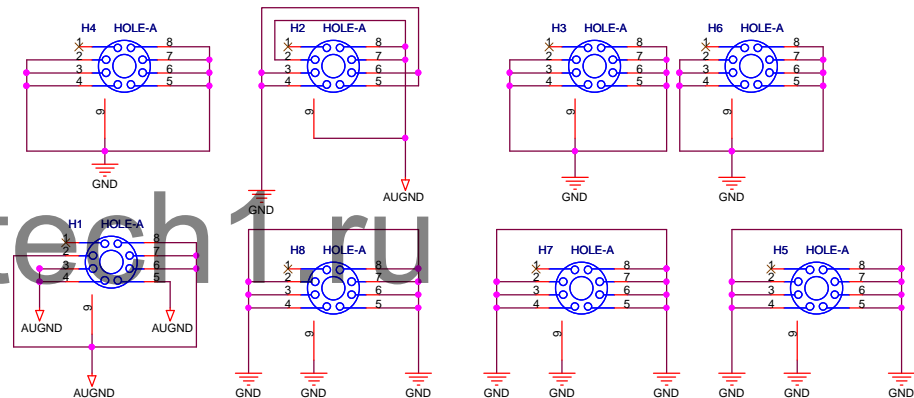
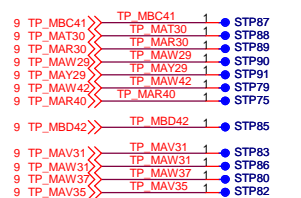
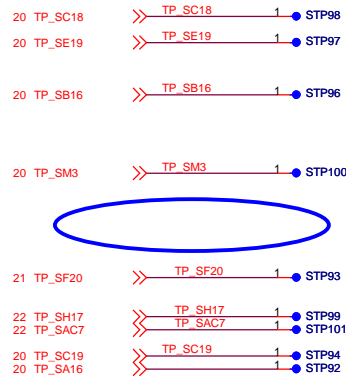
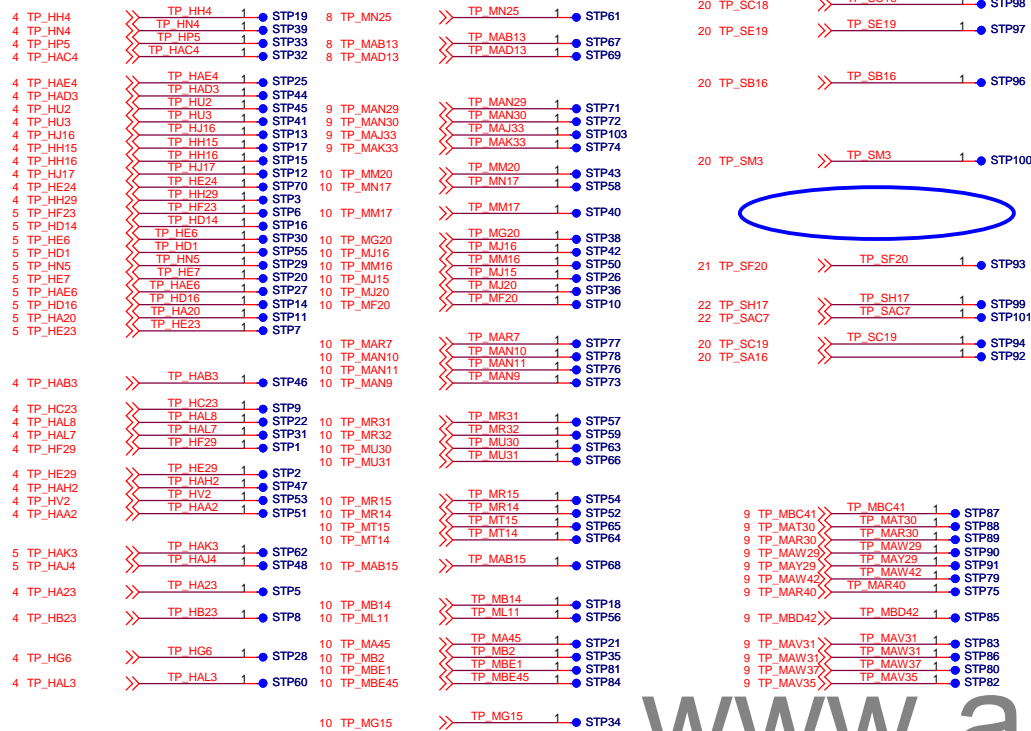


When power off, the ICH_1P5V will be removed

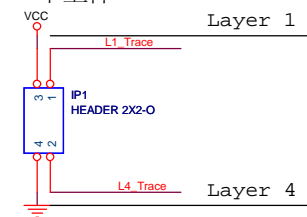


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1080 : trace width 4 mil 50 ohm
 Trace Length 3150 mils
 Spacing: 1.clearance to itself 50/4/50(S:W:S)
 2.clearance to other signal 3W

ATX P/S WITH 1A STBY CURRENT				
5VSB	5V	3.3V	12V	-12V
+/-5%	+/-5%	+/-5%	+/-5%	+/-5%

AO4609

DDR3

ATX4P1	
12V	+/-5%

RT9214 SW POWER1.5V

RT8106A SW POWER1.1V

3.3V LDO REGULATOR 1086

5VAA LDO REGULATOR 78L05

OP_1A
1.1V MOSFET LINEAR TO252

OP_2B
1.5V MOSFET LINEAR TO252

OP_3A
1.1V MOSFET LINEAR TO252

AO4609

X1 PCIe per		PCI Slot (per slot)		X1 PCIe per		X16 PCIe per	
3.3V	3.0A	5V	5.0A	3.3V	3.0A	3.3V	3.0A
12V	0.5A	3.3V	7.6A	12V	0.5A	12V	5.5A
3.3Vaux	0.375A	12V	0.5A	3.3Vaux	0.375A	3.3Vaux	0.375A
		3.3Vaux	0.375A				
		-12V	0.1A				

USB X6 FR		USB X4 IO	
VDD	2.0A	VDD	2.0A
5VDual	2.0A	5VDual	2.0A

Intel 775 CPU		
VCCP		95A
VTT	1.1V	4.6A

DDR3 4DIMMs		
VTT_DDR	0.75V	0.83A
VDD MEM	1.5V	7.2A

Intel EAGLELAKE		
VTT	1.1V	TBD
VCCSM	1.5V	TBD
VCC	1.1V	20A
VCC_EXP	1.1V	TBD
VCC_CL	1.1V	TBD
Misc 3.3V	3.3V	TBD

Intel ICH9/10		
V_CPU_IO 1.1V	1.1V	TBD
VCC1_5A 1.5V	1.5V	1.652A
VCC1_5B 1.5V	1.5V	0.646A
VCC1_1 1.1V	1.1V	1.16A
VCC3 3.3V	3.3V	0.308A
VCCSUS3_3 3.3V	3.3V	0.2A
VCCGLAN 1.5V	1.5V	87mA
VCCGLAN 3.3V	3.3V	1mA
VCCCL 3.3V	3.3V	19mA
VCCLAN_3.3V	3.3V	19mA
VCCDMI 1.1V	1.1V	41mA
VCCRTC 3.3V	3.3V	6uA
VCCHDA 1.5V	1.5V	32mA
VCCHDASUS 1.5V	1.5V	33mA

ETHERNET		
VDDIO	3.3V	15.5mA
VDDC	1.8V	418.2mA
VDDC	1.0V	277.2mA

SUPER I/O		
5VSB	5V	15mA
VCC	5V	60mA
BAT 3.3V	3.3V	2uA

AZALIA		
DVDD 3.3V	3.3V	40mA
AVDD	5V	51mA

BATTERY

